

Superconducting qubits control using FPGAs

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We present an open-source tool designed for control and readout of superconducting qubits using Radio-Frequency System on Chip (RFSoc) FPGA boards. This tool facilitates swift calibration experiments and enables the deployment of gate-based applications.

<https://drive.google.com/open?id=1-bVOiubiMvm4QIlGWbv5PnX9cD5ToiUU>

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